

transparent second substrate, and a liquid crystal layer and a color filter layer sandwiched between said first and second substrates, comprising:

said color filter layer disposed on said first substrate;

said liquid crystal layer disposed between said color filter layer and said second substrate;

plural scan signal electrodes, video signal electrodes for crossing said scan signal electrodes in a matrix form, and plural thin film transistors in association with the crossing points between said scan signal electrodes and said video signal electrodes provided on said first substrate below said color filter layer;

at least one pixel formed in each of areas surrounded by said plural scan signal electrodes and said video signal electrodes;

each pixel provided with a common electrode which is connected over plural pixels through a common electrode wire to supply reference potential, and a pixel electrode which is connected to the corresponding thin film transistor and disposed as to confront said common electrode in said pixel area; and

said common electrode and said pixel electrode disposed between said color filter layer and said liquid crystal layer;

wherein said common electrode and said pixel electrode are disposed in different layers through an interlayer separation film formed of transparent insulating material,

wherein electric field having a component which is dominantly parallel to said first substrate is produced in said liquid crystal layer by applying a voltage across said

common electrode and said pixel electrode, and liquid crystal before the voltage is applied thereto is oriented substantially parallel to said first substrate; and

wherein said interlayer separation film is formed on said common electrode, and said pixel electrode is formed on said interlayer separation film.

4. (Amended) The liquid crystal display device as claimed in claim 1, wherein an overcoat layer for protecting said color filter layer is formed on said color filter layer, and said interlayer separation film is formed on said common electrode, and said pixel electrode is formed on said interlayer separation film.

5. (Amended) The liquid crystal display device as claimed in claim 1, wherein an overcoat layer for protecting said color filter layer is formed on said color filter layer, and said pixel electrode is formed on said overcoat layer.

12. (Amended) The liquid crystal display device as claimed in claim 10, wherein said interlayer separation film is formed on said common electrode, and said pixel electrode is formed on said interlayer separation film.

13. (Amended) The liquid crystal display device as claimed in claim 10, wherein an overcoat layer for protecting said color filter layer is formed on said color filter layer, and

said interlayer separation film is formed on said common electrode, and said pixel electrode is formed on said interlayer separation film.

14. (Amended) The liquid crystal display device as claimed in claim 10, wherein an overcoat layer for protecting said color filter layer is formed on said color filter layer, and said pixel electrode is formed on said overcoat layer.

15. (Amended) The liquid crystal display device as claimed in claim 10, wherein said common electrode is formed in a grid shape so as to surround a pixel; and said pixel electrode is disposed so as to traverse the pixel.

20. (Amended) The liquid crystal display device as claimed in claim 19, wherein the boundary at which the felling direction of the liquid crystal molecules is varied is beforehand formed along two directions in which liquid crystal molecules are felled when a voltage is applied.

21. (Amended) The liquid crystal display device as claimed in claim 19, wherein the boundary at which the felling direction of the liquid crystal molecules is varied is beforehand formed in any one of directions in which liquid crystal molecules are felled when a voltage is applied.

25. (Amended) A method of manufacturing a liquid crystal display device comprising a first substrate, a second transparent second substrate, and a liquid crystal layer and a color filter layer sandwiched between said first and second substrates, comprising the steps of:

forming said color filter layer on said first substrate;

forming said liquid crystal layer between said color filter layer and said second substrate;

forming, on said first substrate below said color filter layer, plural scan signal electrodes, plural video signal electrodes crossing said scan signal electrodes in a matrix form, and plural thin film transistors in association with the crossing points between said scan signal electrodes and said video signal electrodes;

forming at least one pixel in each of areas surrounded by said plural scan signal electrodes and said video signal electrodes;

forming, in each pixel, a common electrode which is connected over plural pixels through a common electrode wire to supply reference potential, and a pixel electrode which is connected to the corresponding thin film transistor and disposed as to confront said common electrode in said pixel area;

disposing said common electrode and said pixel electrode between said color filter layer and said liquid crystal layer, and disposing said common electrode and said pixel electrode in different layers through an interlayer separation film formed of transparent insulating material;